

TurboDebug-PCB

DEBUG SYSTEM FOR INTERCONNECT FAULTS



SYNTEST

The Testability Company

BENEFITS

- Multi-purpose interconnect test and debug tool
 - **Detects** interconnect faults for production testing
 - **Locates** interconnect faults for prototype failure debug
 - **Diagnoses** interconnect faults for field testing
- Easy to use PC-based System, with interactive GUI
- Uses standard IEEE-1149.1 JTAG interface
- Interconnect problems displayed on schematics
- Handles bi-directional pins

PRODUCT DESCRIPTION

The TurboDebug-... family of products from SynTest offer various tools and systems for debugging SoC chips and system boards.

TurboDebug-PCB™ simplifies debugging of interconnect faults on system boards populated with one or more SoCs. It uses the boundary scan chain to detect “open” or “short” faults on the printed circuit board. It operates on any PC that has a PCI slot and runs on Linux as OS.

The TurboDebug-PCB™ comes with an interface board to be plugged into a PCI-slot of a PC, and a “demo” system board, with LEDs and jumpers, to illustrate the interconnect test and debug capabilities/operations of the software tool.

For actual testing, customers need to connect the PC-based interface board via a 9-pin or a 25-pin connector to the boundary scan pins (TDO, TDI, TCK, TMS and TRST) on the system board. Interconnects to peripheral I/O pins can be tested using loop-backs.

Test configurations are easy to set up using pull down menus. The program can be operated either in detection or diagnosis mode. Interconnect problems can be displayed on schematics.

OTHER INFORMATION

The TurboDebug-PCB™ . package, in addition to the software tool, contains an interface board to be plugged into the PCI-slot, a “demo” system board, a power adapter and an interconnection cable.

The TurboDebug-PCB™ complements SynTest’s TurboDebug-SoC™, a low-cost SoC-level DFT prototype debugger, to further reduce silicon debug and diagnosis time.

