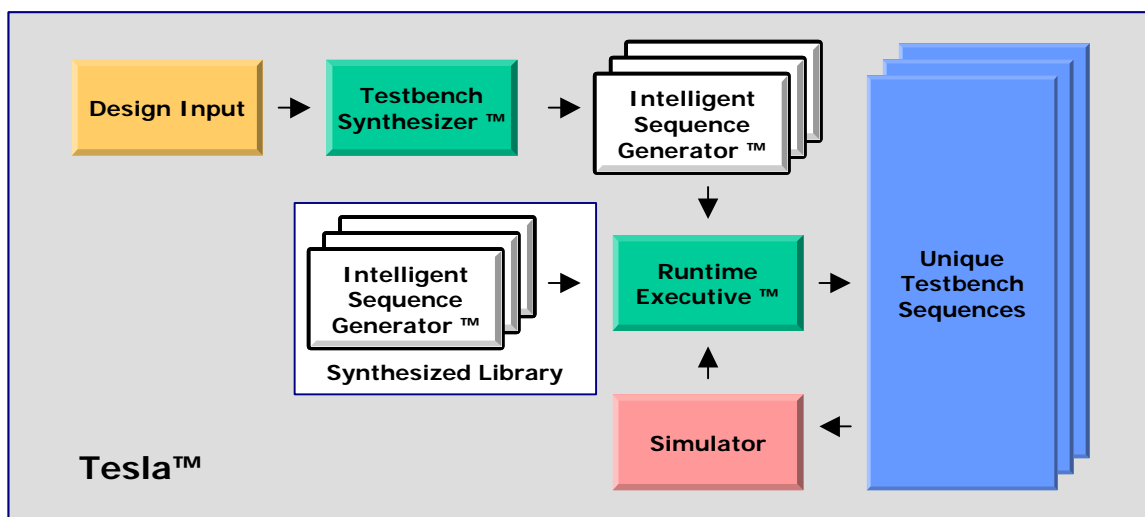




Tesla™ Testbench Synthesizer™

Silicon Forest Research offers EDA software products and services that dramatically reduce the time spent in functional verification, by automating the testbench generation process. Based on innovative technology breakthroughs, SFR's Tesla™ family of tools finds design bugs faster than existing verification tools can, and finds bugs that existing verification tools cannot find at all. The result is a higher degree of confidence in higher quality designs, in much less time.



Tesla Testbench Synthesizer

SFR's Tesla Testbench Synthesizer (TBS) compiles information that describes a design's behavior, and then synthesizes Intelligent Sequence Generators™ in the form of native compiled testbench modules. Using basic constructs in standard C++, a verification engineer can easily create the input to Tesla TBS from a design's specifications and its interface protocols. Unlike other testbench tools, Tesla TBS does not require the verification engineer to write each of the sequences to be tested. Instead, using advanced implication technology, Tesla automatically generates more unique sequences, faster than any other alternative tool or methodology. And Tesla's standard C++ input descriptions are easy to write, and do not require learning a new custom language. Tesla requires far less input from the verification engineer, the format is simpler, and the language is an industry standard. Tesla enables verification engineers to be far more productive, finding design bugs faster than existing tools can, and finding bugs that existing tools cannot find at all.

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