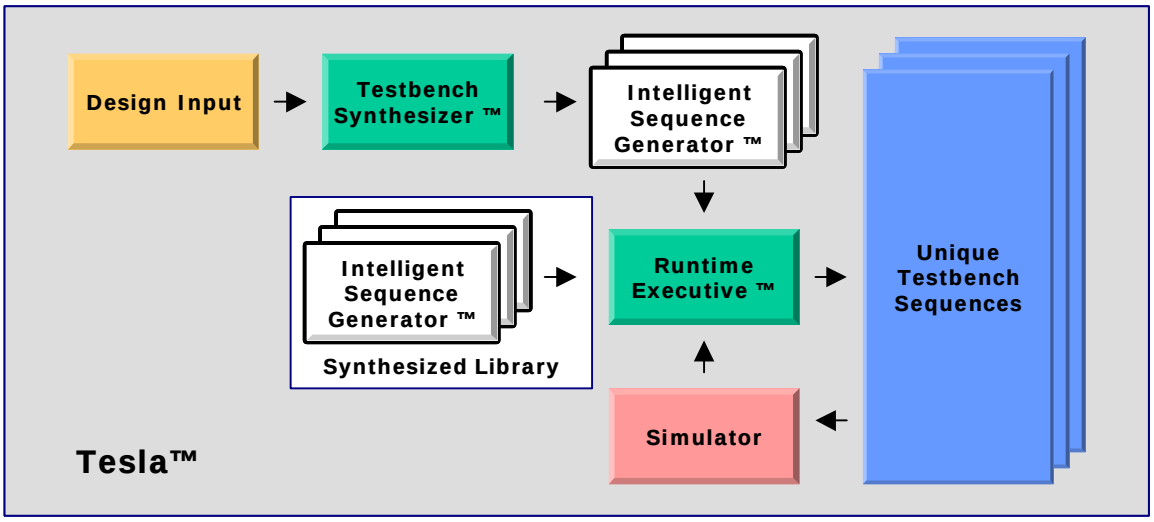
**Silicon Forest Research, Inc.**

**Product Overview**

## Tesla ™ Runtime Executive ™

Silicon Forest Research offers EDA software products and services that dramatically reduce the time spent in functional verification, by automating the testbench generation process. Based on innovative technology breakthroughs, SFR's Tesla™ family of tools finds design bugs faster than existing verification tools can, and finds bugs that existing verification tools cannot find at all. The result is a higher degree of confidence in higher quality designs, in much less time.




### Tesla Runtime Executive

SFR's Tesla Runtime Executive (RTE) manages the execution and interaction between a Verilog simulator and one or more Tesla Intelligent Sequence Generators, during simulation. Unlike other testbench tools, Tesla RTE's utilize only the fastest and most widely supported PLI routines. This enables Tesla testbench sequences to be created with negligible impact on simulation performance. And when multiple Tesla ISG's are generating verification sequences in parallel, Tesla RTE's keep track of all of the design's resources, making sure they are allocated and shared properly between interfaces. If the design hardware is programmable, Tesla interweaves firmware instructions to configure the hardware prior to generating hardware verification sequences. Tesla enables verification engineers to quickly and efficiently test the interactions between multiple subsystems and interfaces, finding design bugs faster than existing tools can, and finding bugs that existing tools cannot find at all.

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